



**SHRI VISHNU ENGINEERING COLLEGE FOR WOMEN: BHIMAVARAM
(AUTONOMOUS)**

(AICTE Approved & Permanently Affiliated to JNTUK, Kakinada)

EXAMINATION BRANCH

II B. TECH - I SEMESTER (R14-R) SUPPLEMENTARY END EXAMINATIONS – NOVEMBER - 2024

T I M E T A B L E

TIME: 9.00 AM – 12.00 Noon

| Branch/Code | 19-11-2024 | 21-11-2024 | 23-11-2024 | 26-11-2024 | 28-11-2024 | 30-11-2024 |
|---|---|--|---|--|---|--|
| | (Tuesday) | (Thursday) | (Saturday) | (Tuesday) | (Thursday) | (Saturday) |
| Civil Engineering (01) | Building Materials and Building Construction (UGCE3T01) | Engineering Mechanics (UGCE3T02) | Strength of Materials – I (UGCE3T03) | Fluid Mechanics – I (UGCE3T04) | Surveying (UGCE3T05) | Probability and Statistics (UGBS3T01) |
| Electrical & Electronics Engineering (02) | Electrical Circuit Analysis – I (UGEE3T01) | Electro Magnetic Fields (UGEE3T02) | Electrical Machines – I (UGEE3T03) | Basic Electronic Devices & Circuits (UGEC3T06) | Thermal & Hydro Prime Movers (UGME3T05) | Complex Variables and Statistical Methods (UGBS3T02) |
| Mechanical Engineering (03) | Fluid Mechanics & Hydraulic Machinery (UGME3T02) | Engineering Mechanics (UGME3T01) | Metallurgy & Material Science (UGME3T03) | Thermodynamics (UGME3T04) | Electrical & Electronics Engineering (UGEE3T05) | --- |
| Electronics & Communication Engineering (04) | Electronic Device & Circuits (UGEC3T01) | Network Analysis (UGEC3T02) | Digital Logic Design (UGEC3T03) | Signals & Systems (UGEC3T04) | Electrical Technology (UGEE3T04) | Random Variables & Stochastic Processes (UGEC3T05) |
| Computer Science & Engineering (05) | Mathematical Foundations of Computer Science (UGCS3T01) (Common to CSE & IT) | Data Structures (UGCS3T03) (Common to CSE & IT) | Digital Logic Design (UGCS3T02) (Common to CSE & IT) | Object Oriented Programming through C++ (UGCS3T04) (Common to CSE & IT) | Theory of Computation (UGCS3T05) | Software Engineering (UGCS3T06) |
| Information Technology (12) | Mathematical Foundations of Computer Science (UGCS3T01) (Common to CSE & IT) | Data Structures (UGCS3T03) (Common to CSE & IT) | Digital Logic Design (UGCS3T02) (Common to CSE & IT) | Object Oriented Programming through C++ (UGCS3T04) (Common to CSE & IT) | Data Communications (UGIT3T01) | --- |

Khay
Controller of Examinations

Shri Vishnu Engineering College of Women(A)
Vishnupur, BHIMAVARAM-534 202.

Copy to: All HODs, Office, Class Room Circulation, Notice Boards
Date: 29-10-2024

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